*R*

*Y*

*B*

*Q5*

*Q6*

*Q7*

*Q8*

*Vdc2*

*R5*

*R6*

*R7*

*R8*

*Vdc2*

*Q1*

*Q2*

*Q3*

*Q4*

*Vdc1*

*R1*

*R2*

*R4*

*Vdc1*

*P1*

*P2*

*P3*

*P4*

*P5*

*P6*

*P7*



*P8*

*R3*

*Vdc1*

*Vdc2*

*Q9*

*Q10*

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*R12*

*Vdc3*

*P9*

*P10*

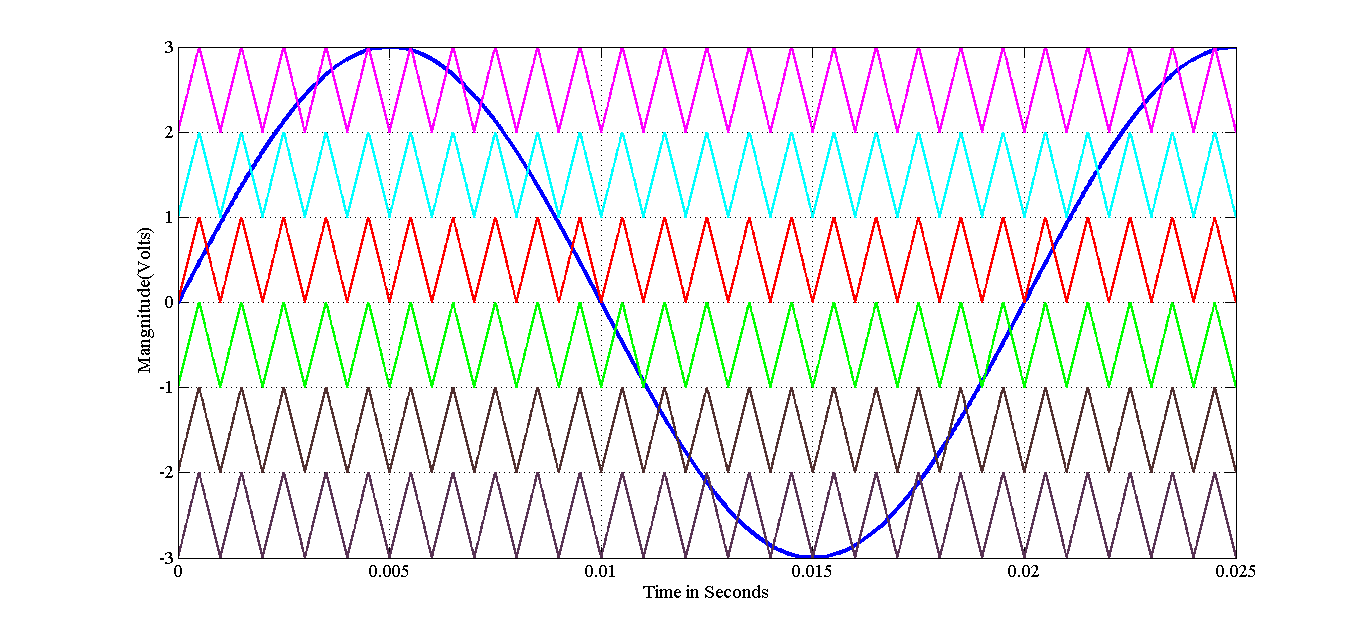
*P11*

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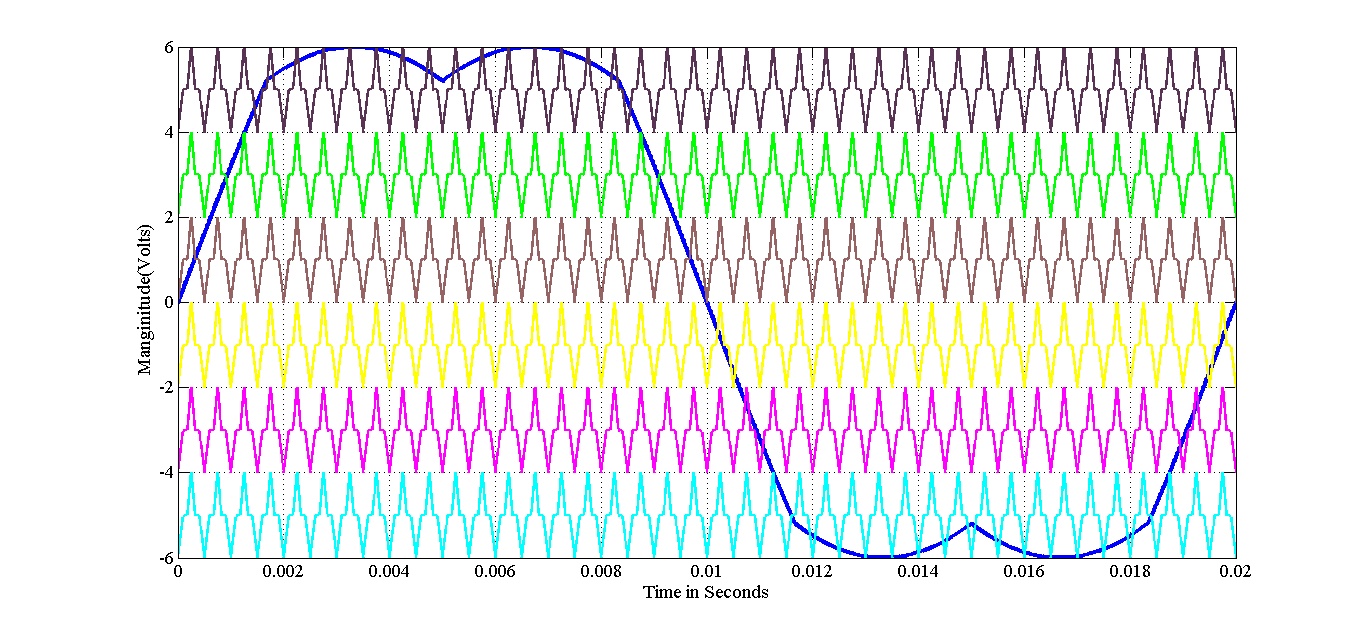
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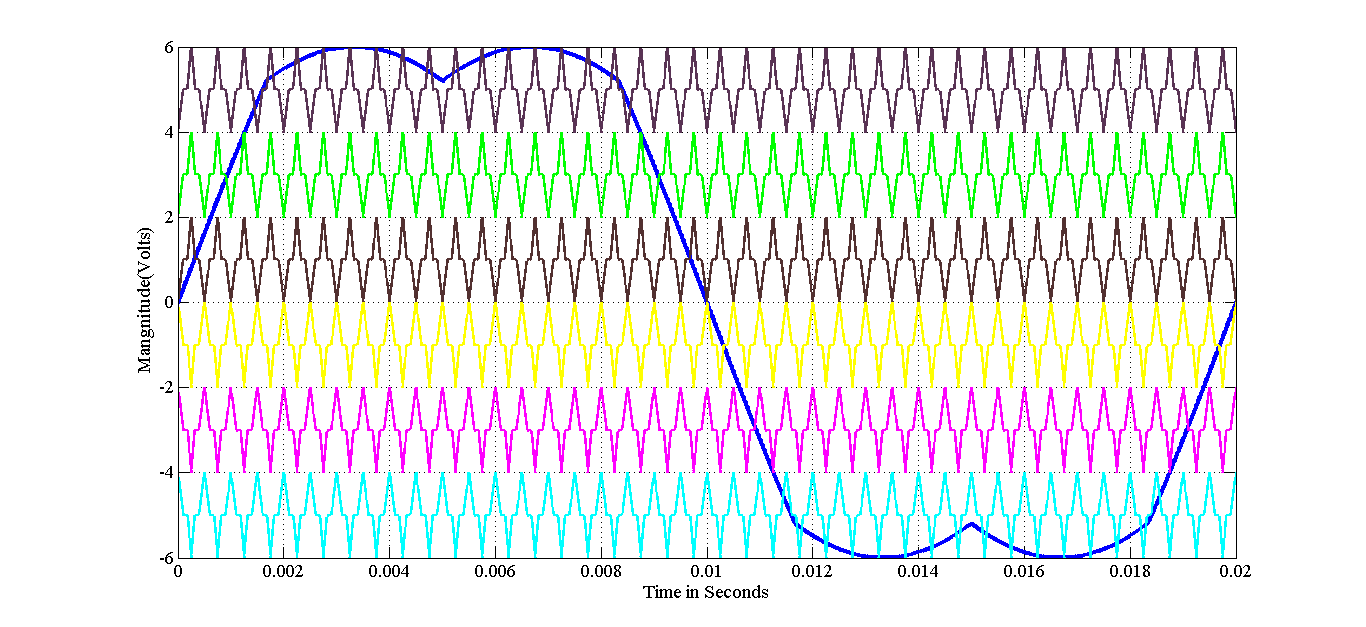
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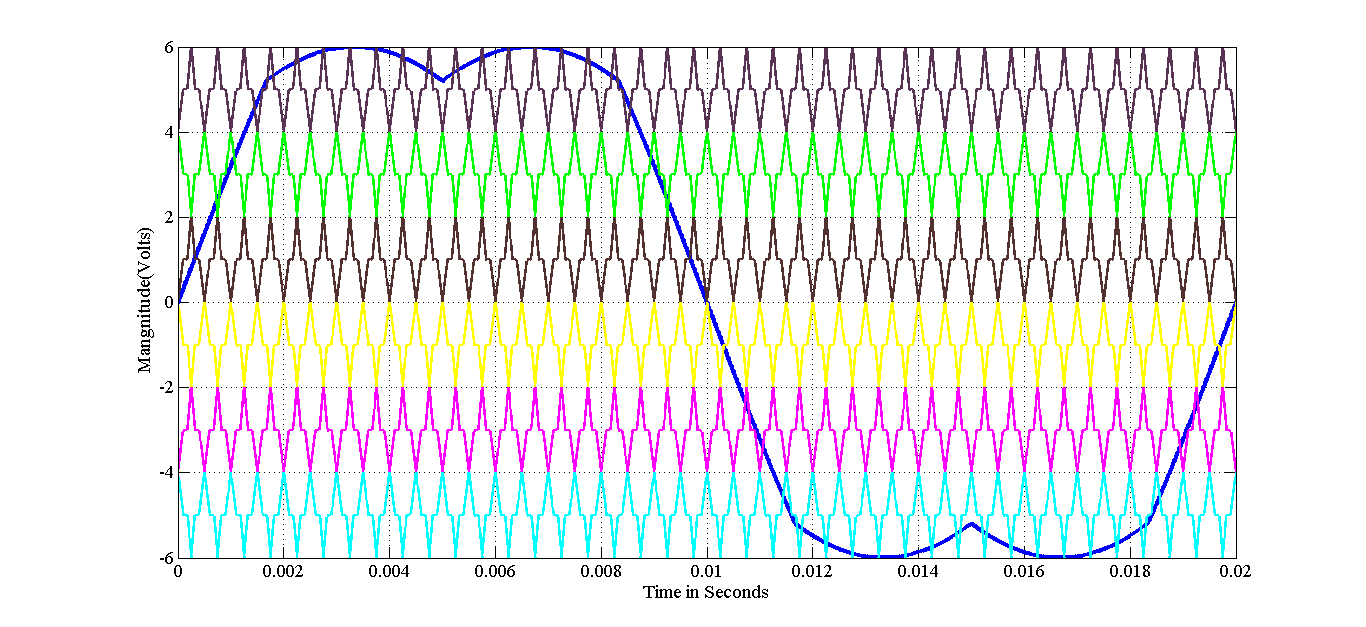
PDSPWM\_7LEVEL



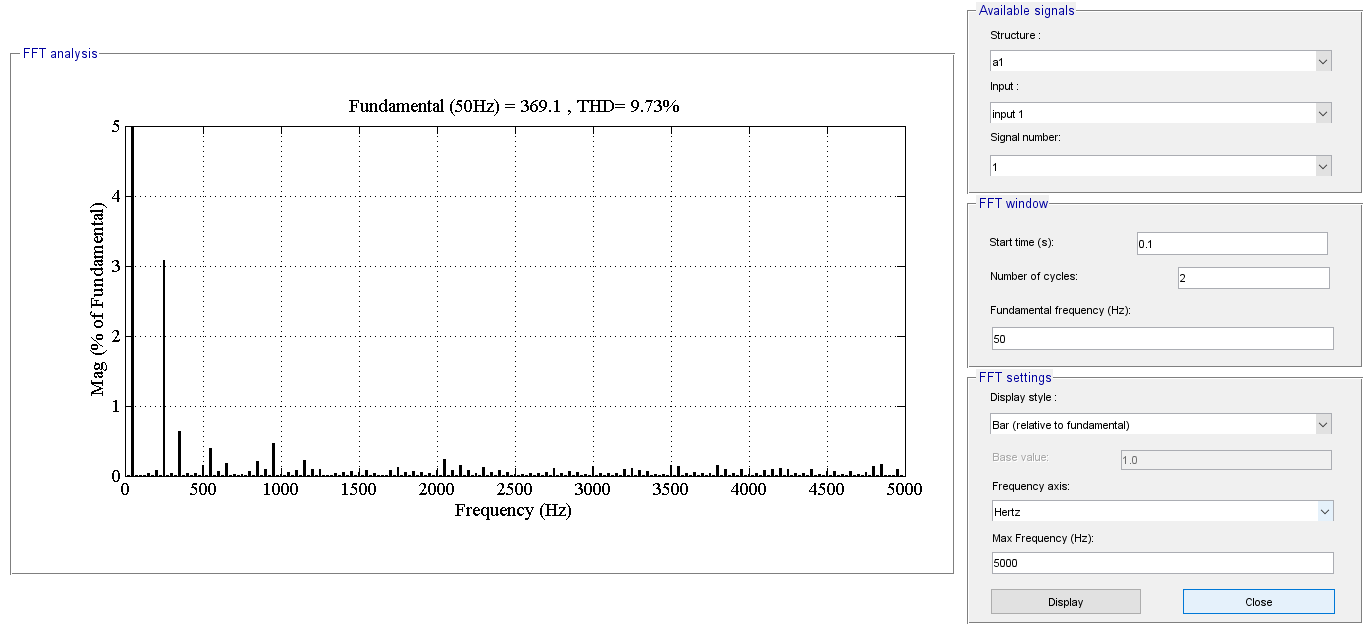
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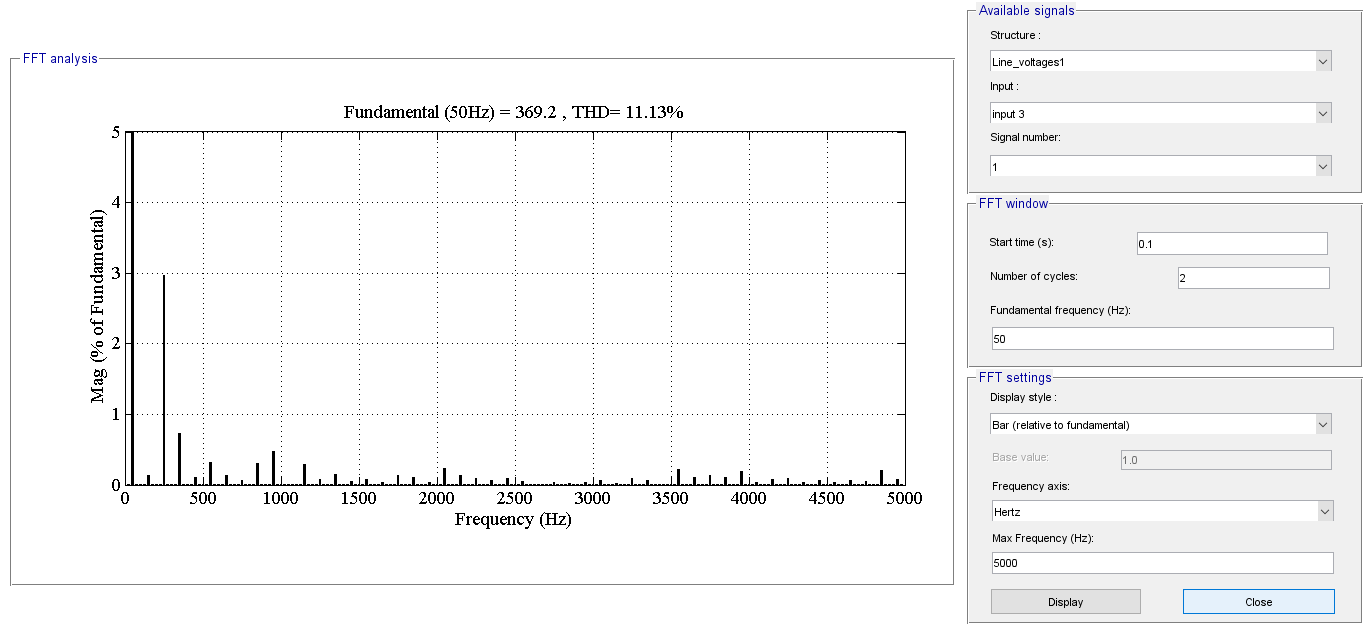
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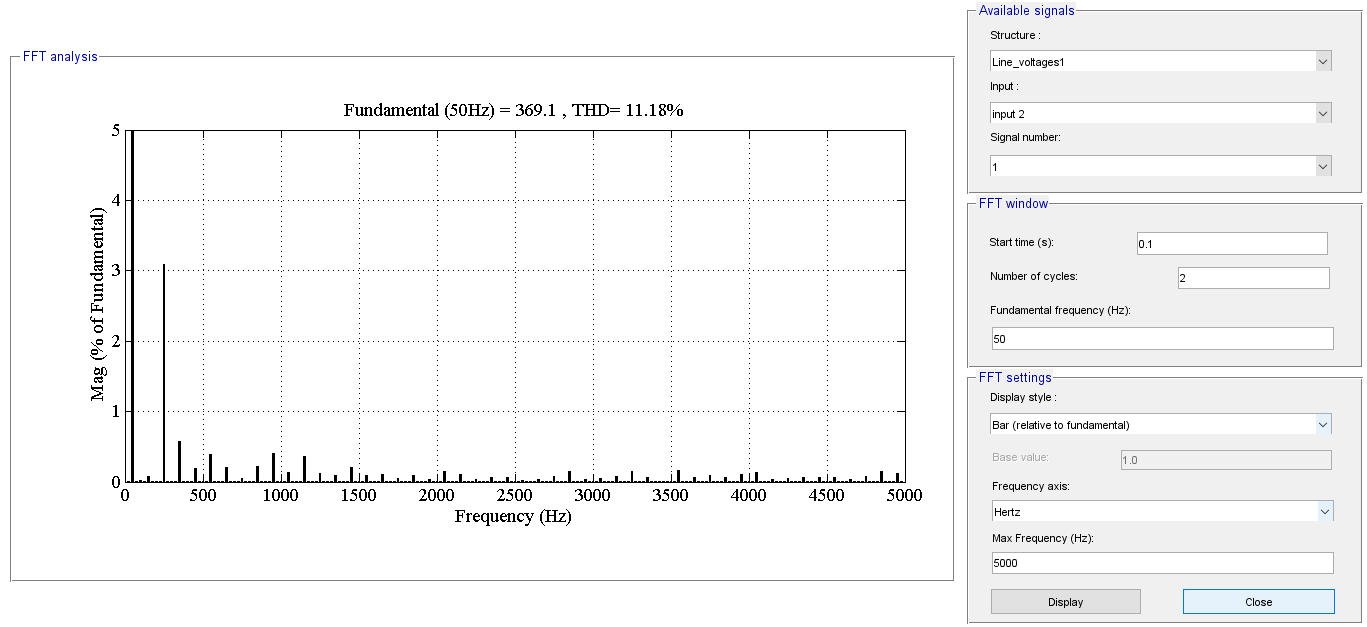
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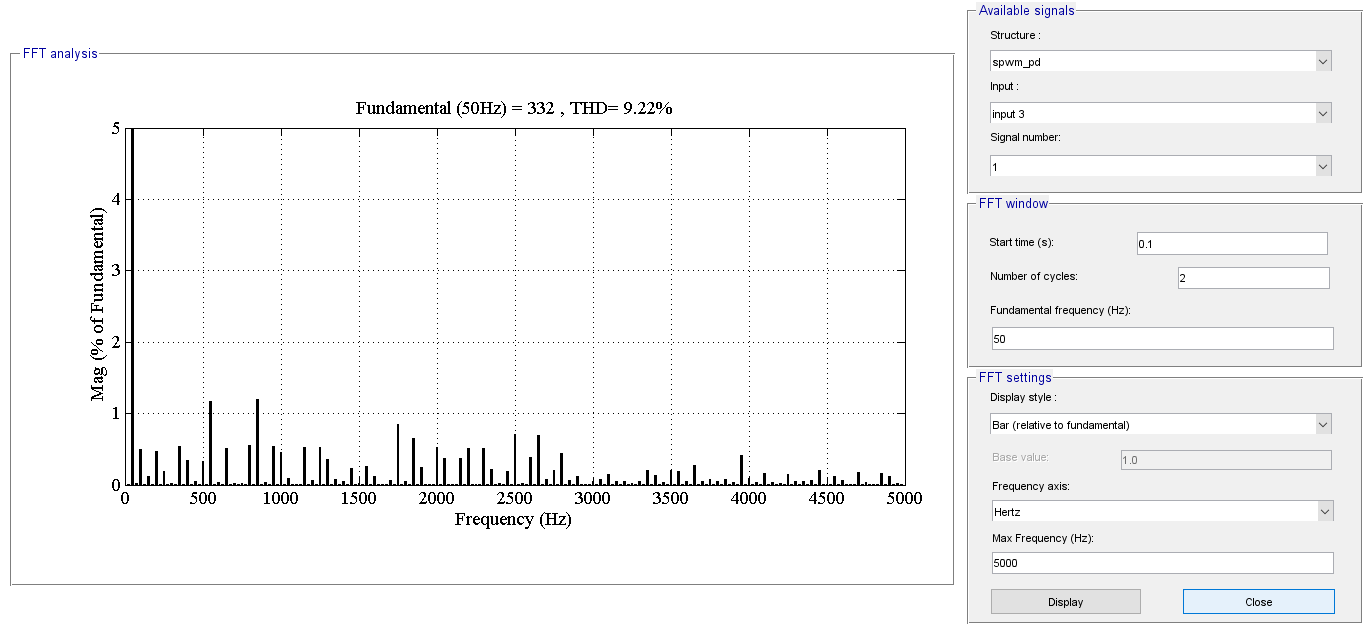
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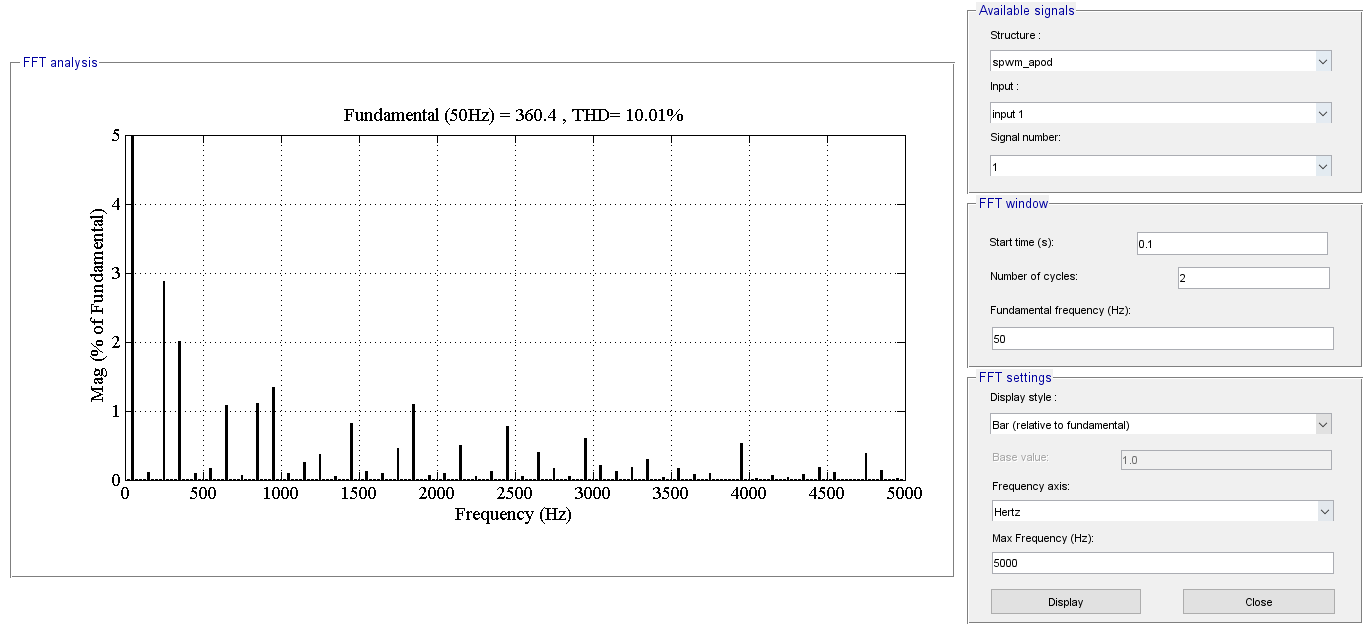
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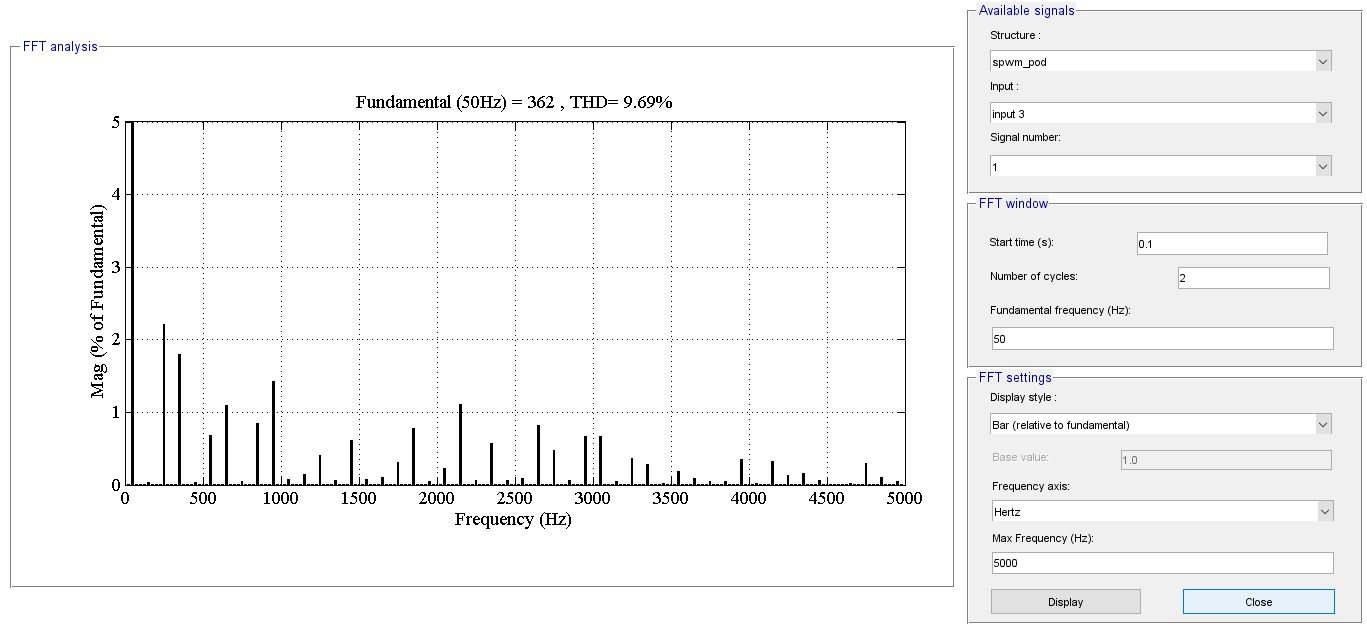
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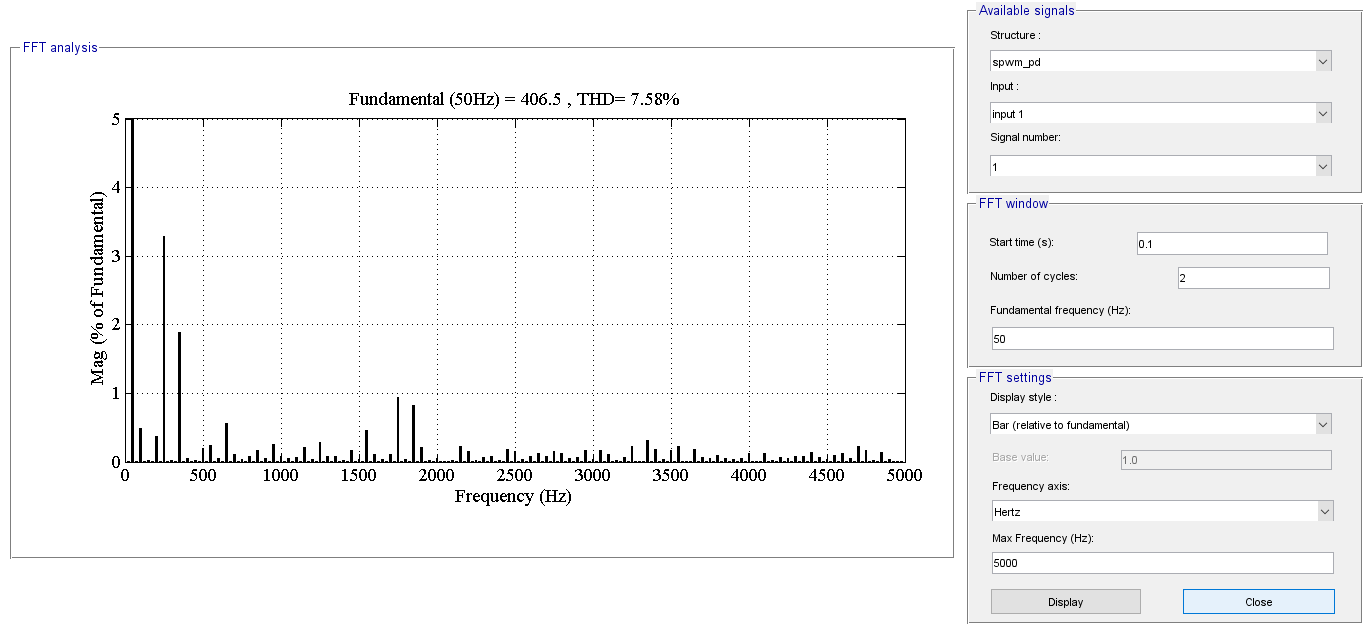
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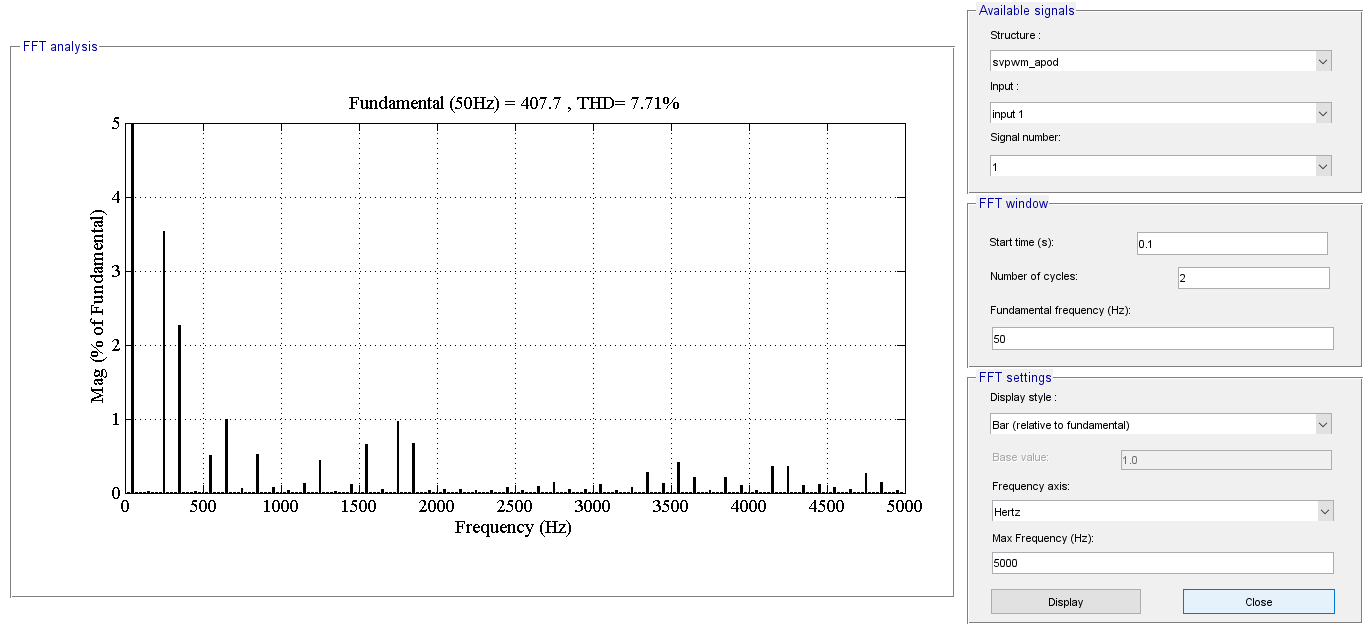
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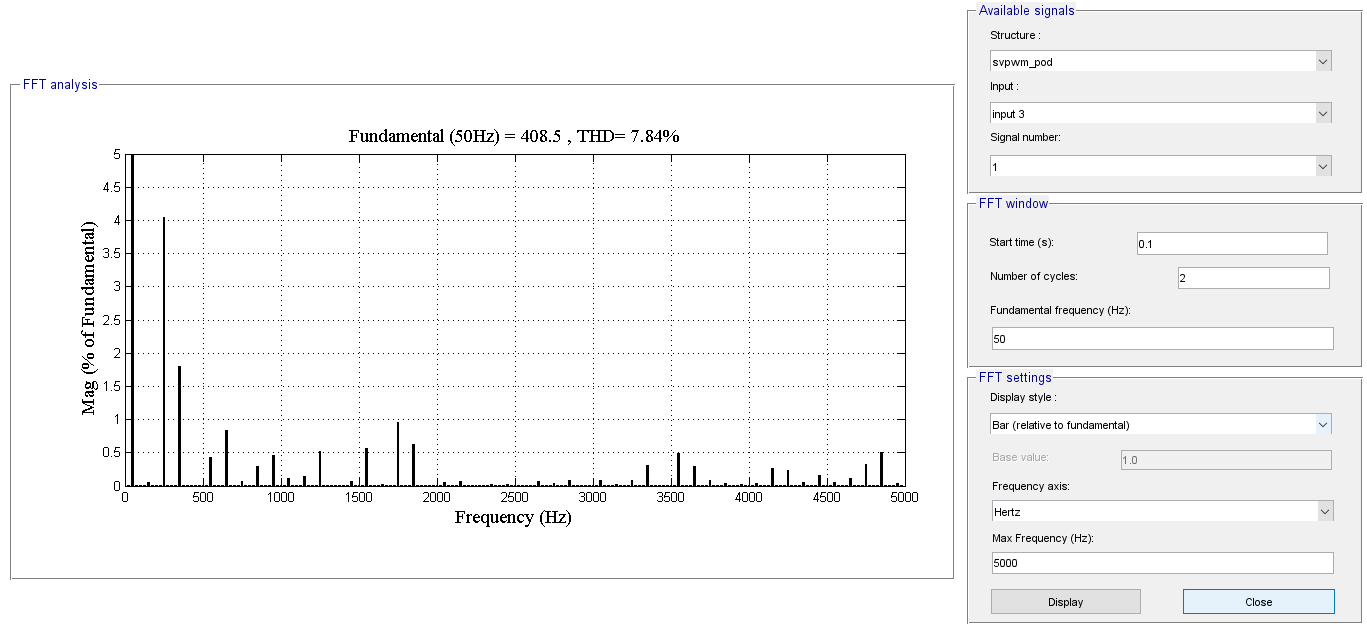
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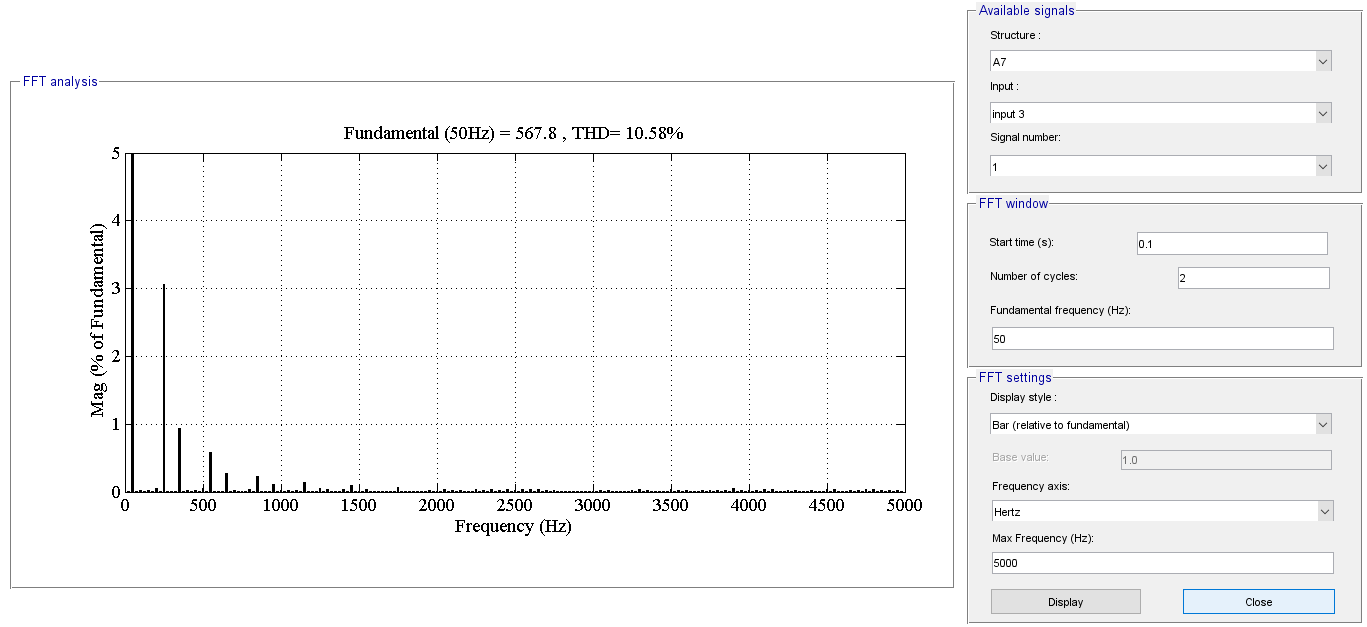
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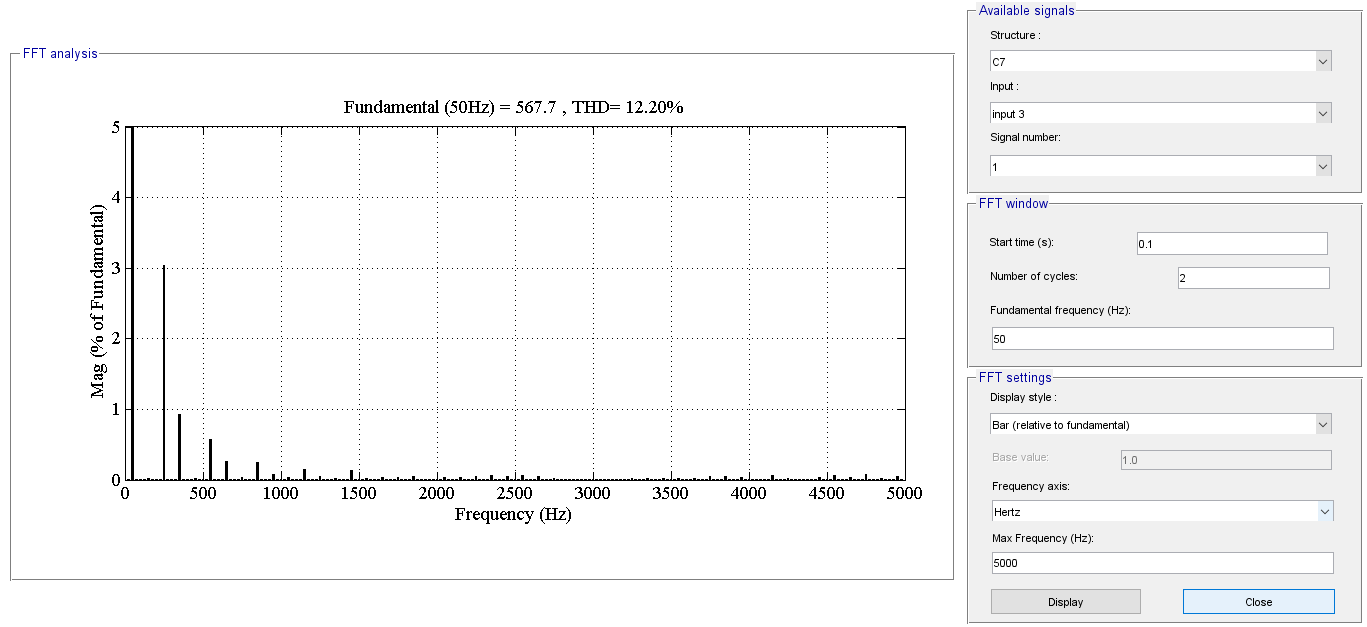
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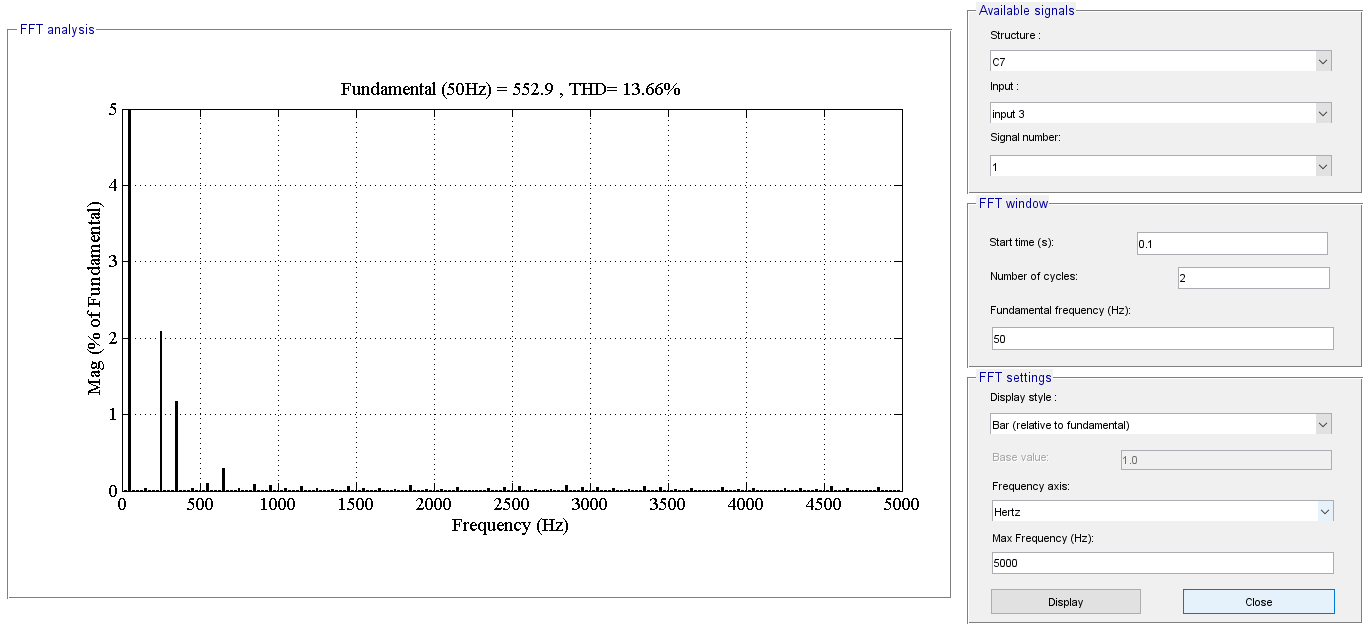
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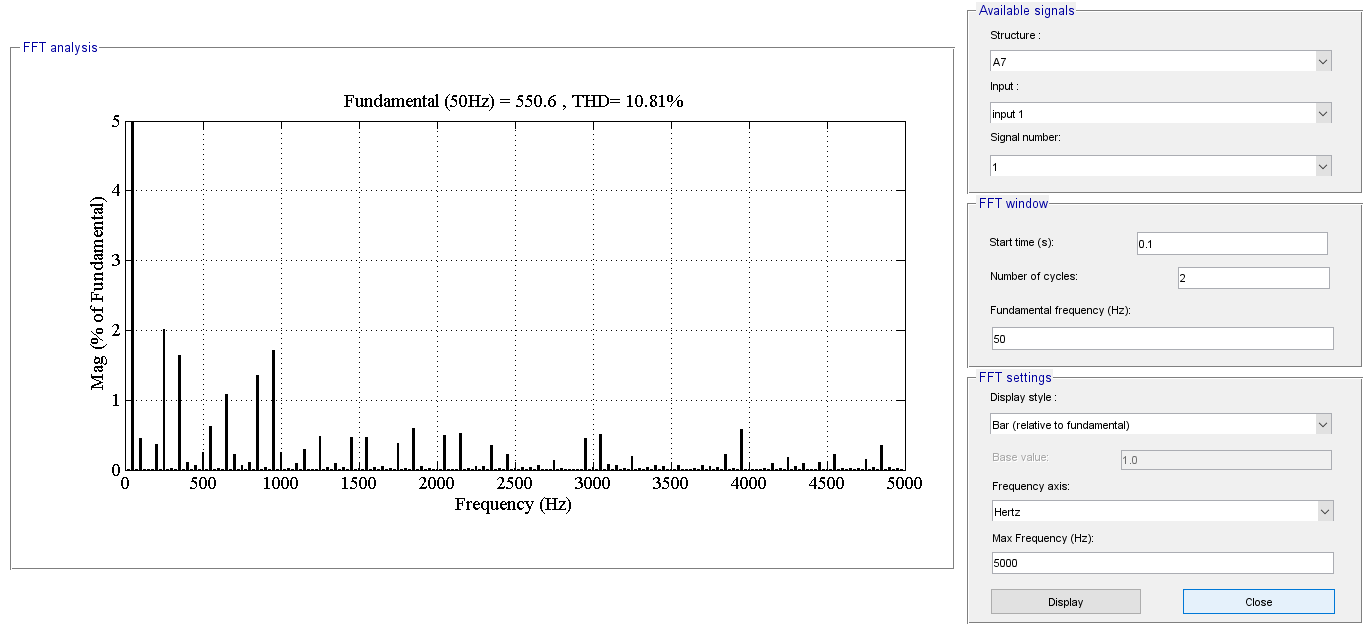
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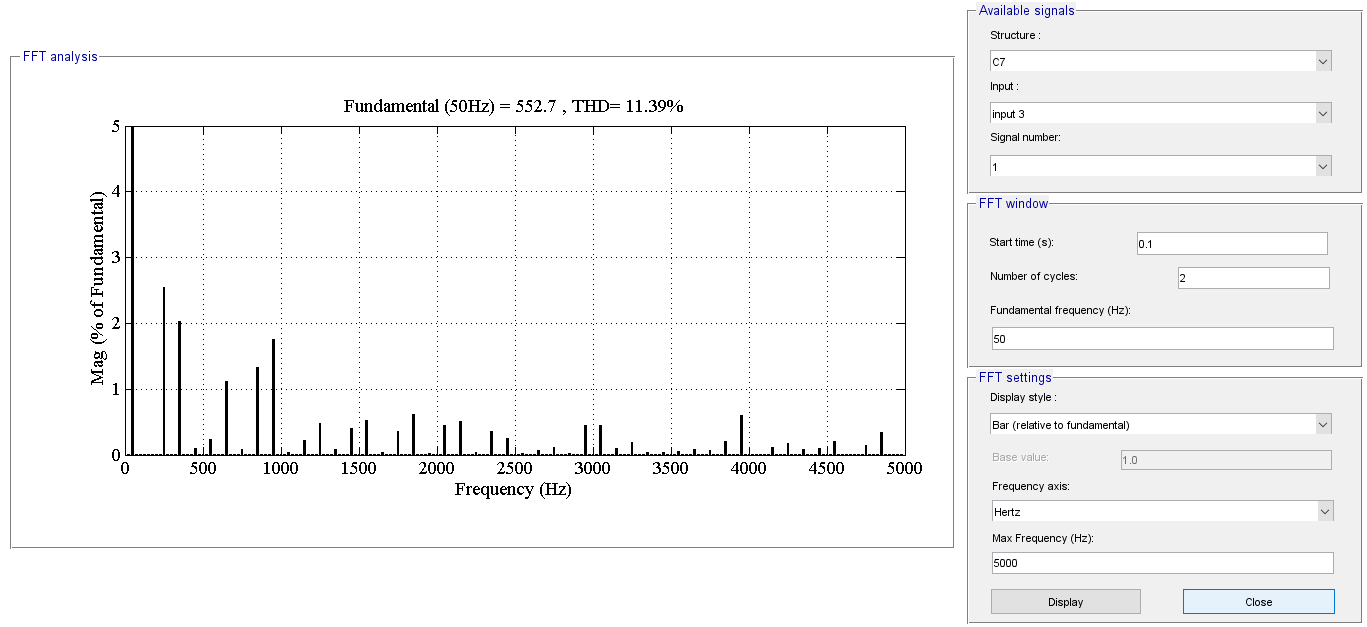
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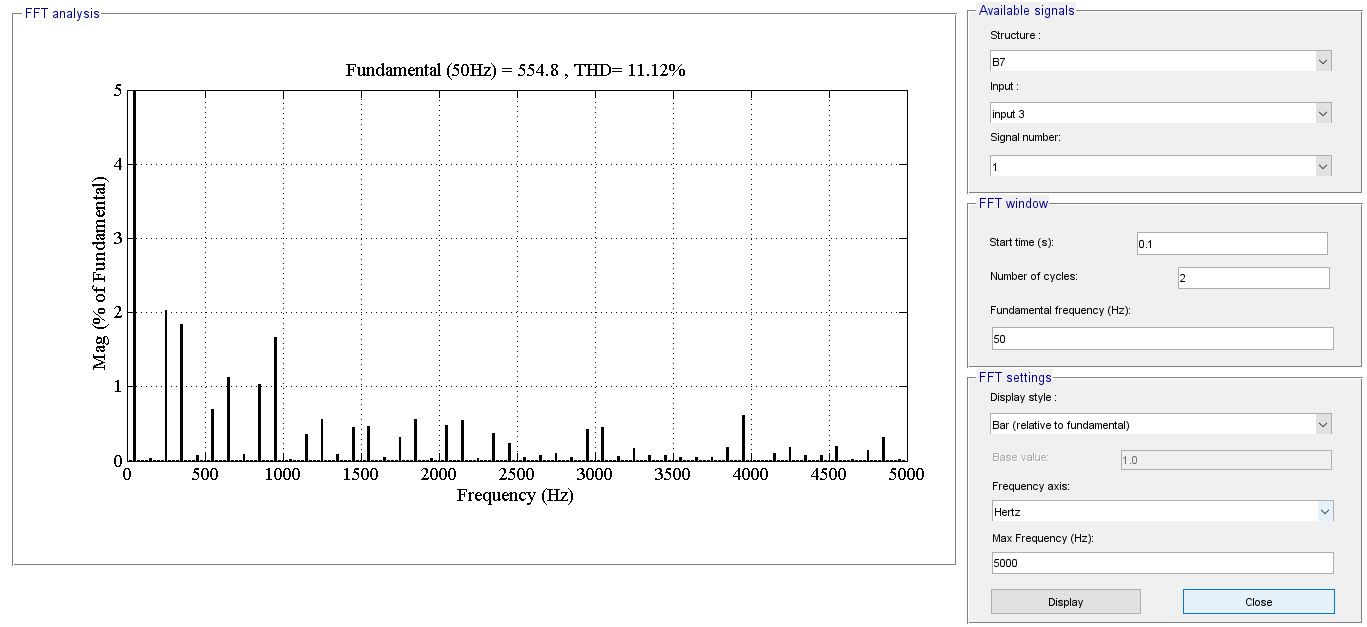
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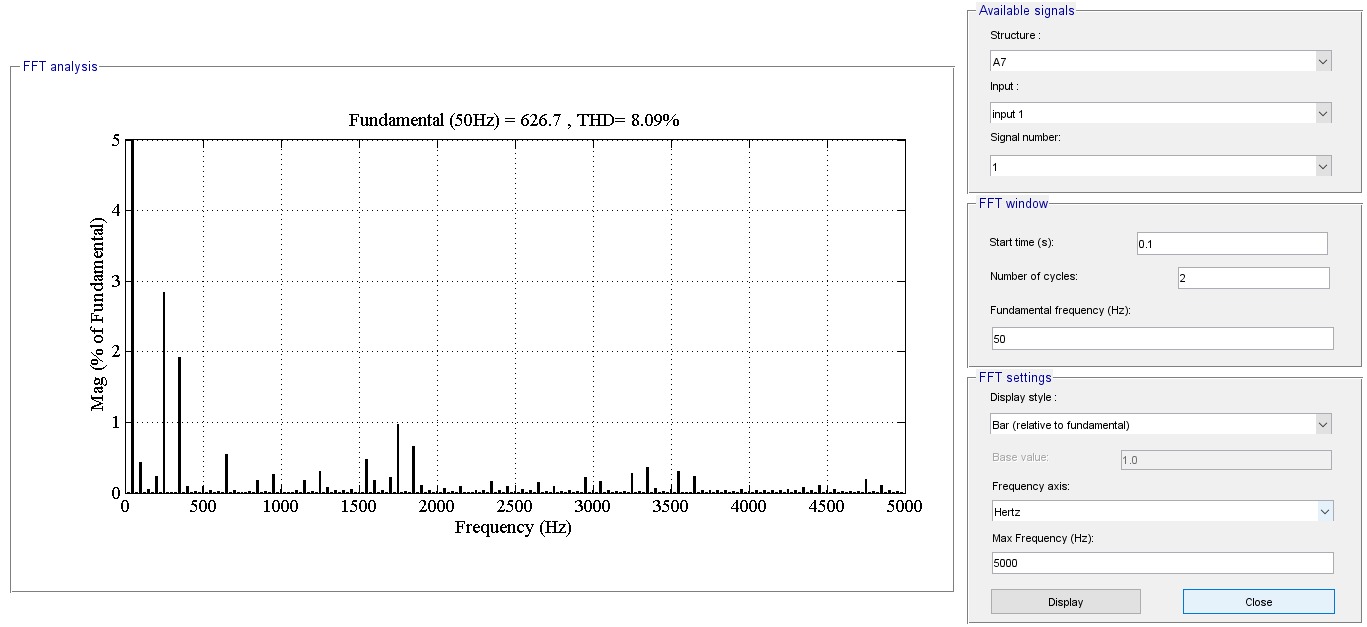
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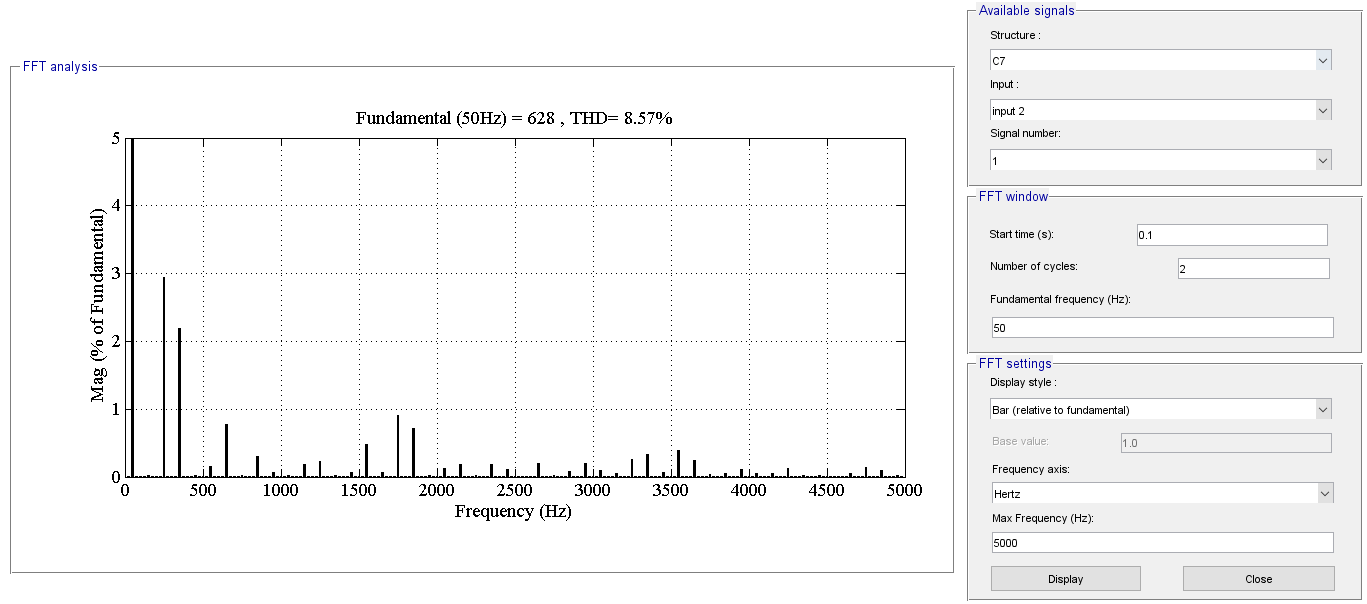
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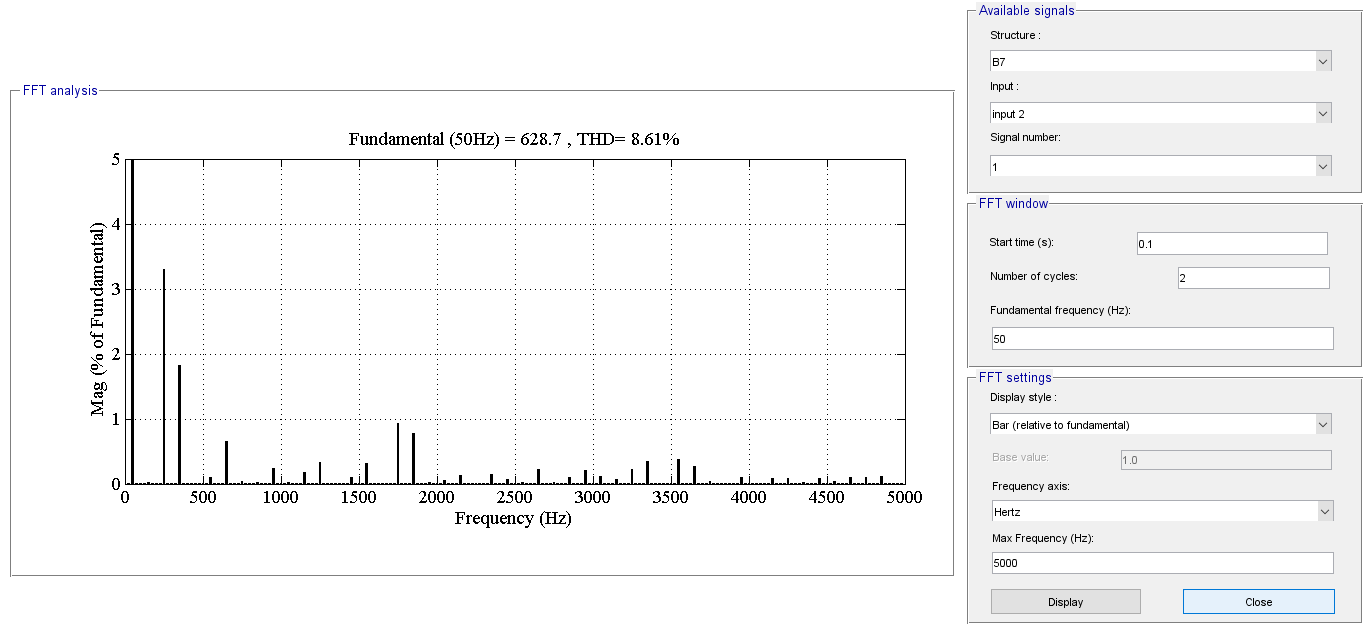
CHB\_TTMC\_PODSPWM



CHB\_TTMC\_PDSVPWM



CHB\_TTMC\_APODSVPWM



CHB\_TTMC\_PODSVPWM